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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Applicant** 

Charles Paul Siska, Jr.

Application No.

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**AUTOMATED TRANSLATION OF A** 

MICROPROCESSOR OPCODE SUMMARY TABLE TO AN ARCHITECTURE DESCRIPTION LANGUAGE

Art Unit

: 2124

Docket No.

: 210149.412

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Mail Stop RCE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## INFORMATION DISCLOSURE STATEMENT

## Commissioner for Patents:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicant wishes to make known to the U.S. Patent and Trademark Office the references set forth on the attached Form PTO-1449 (copies of the cited references are enclosed). As to any reference supplied, applicant does not admit that it is "prior art" under 35 U.S.C. §§ 102 or 103, and specifically reserves the right to traverse or antedate any such reference, as by a showing under 37 C.F.R. § 1.131 or other method. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicant's duty to disclose all information he is aware of which is believed relevant to the examination of the above-identified application, applicant believes that his invention is patentable.